

INTERFERENCE  
**EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L15	0	((((delay or tim\$3) near4 budget) near5 (minimum or maximum) and ((long\$3 or far) near5 (path or wir\$3 or distance or rout\$3 or connec\$4 or interconnect\$4 or segment)) and ((short\$3 or neighbor\$4 or adjacent or nadjoint\$3 or clos\$3) near6 (path or wir\$3 or distance or rout\$3 or connect\$3 or interconnect\$4 or segment))) and implement\$3 and (delay same maximum same (tim\$4 or budget)) and rout\$4).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 11:26

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	13111	delay same maximum same (tim\$4 or budget) and rout\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 10:59
L2	7337	delay same maximum same (tim\$4 or budget) and rout\$4 and (delay same (long\$4 or short\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 10:33
L3	1279	delay same maximum same (tim\$4 or budget) and rout\$4 and (delay same (long\$4 or short\$4)) and constraint	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 10:33
L4	1279	(delay same maximum same (tim\$4 or budget)) and rout\$4 and (delay same (long\$4 or short\$4)) and constraint	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 10:33
L5	182	(delay same maximum same (tim\$4 or budget)) and rout\$4 and (delay same (long\$4 or short\$4)) and constraint and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 10:33
L6	108	(delay same maximum same (tim\$4 or budget)) and (delay same minimum same tim\$4 or budget) and rout\$4 and (delay same (long\$4 or short\$4)) and constraint and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 10:33
L7	2831	(delay or tim\$3) near4 budget	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 11:01
L8	101	((delay or tim\$3) near4 budget) near5 (minimum or maximum)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 11:02

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L9	19	((delay or tim\$3) near4 budget) near5 (minimum or maximum) and (long\$3 or far) near5 (path or wir\$3 or distance or rout\$3 or connec\$4 or interconnect\$4 or segment)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 11:14
L10	19	((delay or tim\$3) near4 budget) near5 (minimum or maximum) and ((long\$3 or far) near5 (path or wir\$3 or distance or rout\$3 or connec\$4 or interconnect\$4 or segment))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 11:15
L11	12	((delay or tim\$3) near4 budget) near5 (minimum or maximum) and ((long\$3 or far) near5 (path or wir\$3 or distance or rout\$3 or connec\$4 or interconnect\$4 or segment)) and ((short\$3 or neighbor\$4 or adjacent or nadjoint\$3 or clos\$3) near6 (path or wir\$3 or distance or rout\$3 or connect\$3 or interconnect\$4 or segment))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 11:18
L12	7	(((delay or tim\$3) near4 budget) near5 (minimum or maximum) and ((long\$3 or far) near5 (path or wir\$3 or distance or rout\$3 or connec\$4 or interconnect\$4 or segment)) and ((short\$3 or neighbor\$4 or adjacent or nadjoint\$3 or clos\$3) near6 (path or wir\$3 or distance or rout\$3 or connect\$3 or interconnect\$4 or segment))) and (delay same maximum same (tim\$4 or budget)) and rout\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 11:22
L15	0	(((delay or tim\$3) near4 budget) near5 (minimum or maximum) and ((long\$3 or far) near5 (path or wir\$3 or distance or rout\$3 or connec\$4 or interconnect\$4 or segment)) and ((short\$3 or neighbor\$4 or adjacent or nadjoint\$3 or clos\$3) near6 (path or wir\$3 or distance or rout\$3 or connect\$3 or interconnect\$4 or segment))) and implement\$3 and (delay same maximum same (tim\$4 or budget)) and rout\$4).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 11:26

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1. **Simultaneous short-path and long-path timing optimization for FPGAs**  
Fung, R.; Betz, V.; Chow, W.;  
[Computer Aided Design, 2004. ICCAD-2004. IEEE/ACM International Conference on](#)  
7-11 Nov. 2004 Page(s):838 - 845

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